FORM PTO-1449 (Rev. 2-32) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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ATTY. DOCKET NO.	SERIAL NO.
Jalaleddine 1-5	10/694,246 ~~Not yet assigned~~
APPLICANT	
Sateh M. Jalaleddine and Suharli Tedja	
FILING DATE	GROUP
Not yet assigned	2816 ~~Not yet assigned~~

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
KSW	AA	US 2002/0175761 A1	11/2002	Bach et al.	330	277	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	TRANSLATION NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

KSW	AB		"Analysis and Design of Analog Integrated Circuits" by Paul R. Gray et al., Copyright 2001 John Wiley & Sons, Inc., pp. 213-215.

EXAMINER	WELLS	DATE CONSIDERED	02/15/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

992.1109

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